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U.S. PATENT & TRADEMARK OFFICE
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IN AN APPLICATION
JUL 27 2001
July 25, 2001
(use several sheets if necessary)

APPLICANT
Oğuz TanrikuluFILING DATE
March 19, 2001GROUP
2631

U.S. PATENT DOCUMENTS

EXAM- INER INI- TIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB- CLASS	FILING DATE IF APPROPRIATE
	AA				RECEIVED		
	AB				JUL 31 2001		
	AC				Technology Center 2600		
	AD						
	AE						
	AF						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB- CLASS	TRANSLATION YES NO
	AL						
	AM						
	AN						
	AO						

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

<i>J. H.</i>	AR	Mitra, S.K. and Hirano, K., "Digital All-Pass Networks," <i>IEEE Trans. Circuits Systs.</i> , CAS-21(5): 688-700 (1974)
<i>J. H.</i>	AS	Tanrikulu, O. and Kalkan, M., "Design and Discrete Re-optimization of All-pass Based Power Symmetric IIR Filters," <i>Electronics Letters</i> , 32(16): 1458-1460 (1996)
<i>J. H.</i>	AT	Tanrikulu, O., "Adaptive Signal Processing Algorithms with Accelerated Convergence and Noise Immunity," Ph.D. Thesis, Imperial College of Science, Technology and Medicine, University of London (1995)
<i>J. H.</i>	AU	Valenzuela, R.A and Constantinides, A.G., "Digital Signal Processing Schemes for Efficient Interpolation and Decimation," <i>IEE Proceedings-G, Circuits Devices and Systems</i> , 130(6): 225-235 (1983)

EXAMINER	<i>Jeffrey Lloyd</i>	DATE CONSIDERED
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2/1/2013